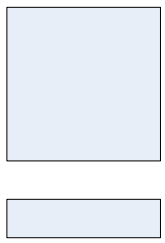
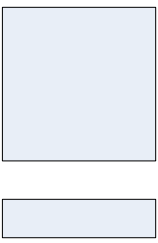
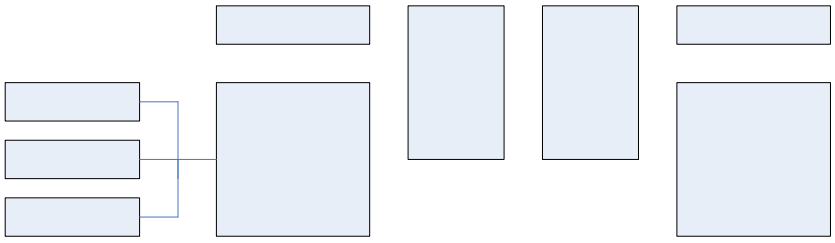


Multiprocessor template based on DMA

Introduction



2. Design a two-port DMA controller and replace one FIFO

